L Number	Hits	Search Text	DB	Time stamp
309	11	("3191100" "3457639" "3516155"	USPAT	2003/07/21 13:47
		"3540121" "3648121" "3931922"		
		"4371912" "4698662" "4957882"		
_	_	"4979289" "5182632").PN.		
310	6	5561328.URPN.	USPAT	2003/07/21 13:49
386	9037	, ·	USPAT;	2003/07/21 13:58
		chip) near2 (alignment or align or aligning	US-PGPUB;	
		or positioning or position or placement or	EPO; JPO;	
		placing or placed or place or locating or	DERWENT;	
		location or locater or aligner or positioner	IBM_TDB	
		or attach or attachment or attaching or		
		attached or affixing or affixed or affix)		
		near5 (frame or template or socket or grid or opening or aperture or matrix)		
387	405	(die or chip) adj (alignment or align or	HODAT.	2002/07/21 14:40
367	405	aligning or positioning or position or	USPAT; US-PGPUB;	2003/07/21 14:48
		placement or placing or placed or place or		
		locating or location or locater or aligner	EPO; JPO;	
		or positioner or attach or attachment or	DERWENT; IBM TDB	
		attaching or attached or affixing or affixed	TBM_TDB	
		or affix) near3 (frame or template or socket		
		or grid or opening or aperture or matrix)		
		and (optical near2 circuit or oic or laser		
		or led or oled or mirror)		
388	320	· ·	USPAT;	2003/07/21 14:00
	323	aligning or positioning or position or	US-PGPUB;	1 -555, 57, 21 14.00
		placement or placing or placed or place or	EPO; JPO;	1
		locating or location or locater or aligner	DERWENT;	
		or positioner or attach or attachment or	IBM TDB	
		attaching or attached or affixing or affixed	_	
		or affix) near3 (frame or template or socket		
		or grid or matrix) and (optical near2		
		circuit or oic or laser or led or oled or		
		mirror)	İ	
389	208	,	USPAT;	2003/07/21 14:05
		aligning or positioning or position or	US-PGPUB;	
		placement or placing or placed or place or	EPO; JPO;	
		locating or location or locater or aligner	DERWENT;	
		or positioner or attach or attachment or	IBM_TDB	
		attaching or attached or affixing or affixed		
		or affix) near2 (frame or template or socket		
		or grid or matrix) and (optical near2		
		circuit or oic or laser or led or oled or		
393	199	mirror)	IIODAE.	0000/05/05 54 05
393	199	(die or chip) adj (alignment or align or aligning or positioning or position or	USPAT;	2003/07/21 14:06
		placement or placing or placed or place or	US-PGPUB;	
		locating or location or locater or aligner	EPO; JPO; DERWENT;	
		or positioner or attach or attachment or	IBM TDB	
		attaching or attached or affixing or affixed	1511_106	
		or affix) near2 (frame or template or socket		
		or grid or matrix) and (optical near2		
		circuit or oic or laser or light near2 diode		
		or oled or mirror)		
394	4	(US-6285064-\$ or US-6209532-\$ or	USPAT	2003/07/21 14:38
		US-5890599-\$ or US-5561328-\$).did.		2003, 01, 22 21130
395	955	257/676.ccls.	USPAT	2003/07/21 14:38
396	150	257/676.ccls. and (LED or oic or oled or	USPAT	2003/07/21 14:41
		laser or light near2 emitting or optical		
		near2 circuit or photonic near2 circuit)	1	
397	36	(die or chip) adj (alignment or align or	USPAT;	2003/07/21 14:55
		aligning or positioning or position or	US-PGPUB;	
		placement or placing or placed or place or	EPO; JPO;	
		locating or location or locater or aligner	DERWENT;	
		or positioner or attach or attachment or	IBM_TDB	
		attaching or attached or affixing or affixed	_	1
		or affix) near3 (frame or template or socket		
i i		or grid or matrix) near5 (opening or		
	1			i e
		aperture or depression or divot or recess or recessed or depressed)		

Page 1

398	784	257/433.ccls.	USPAT;	2003/07/21 14:55
			US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
			IBM_TDB	0000/05/05
399	235	257/433.ccls. and (alignment or aligned or	USPAT;	2003/07/21 14:57
		aligning or positioning or placement)	US-PGPUB;	
			EPO; JPO;	
			DERWENT;	
100	116	257/422 gala and /anomhuma on onomina or	IBM_TDB USPAT;	2003/07/21 15:11
400	110	257/433.ccls. and (aperture or opening or recess or recessed or template or frame or	US-PGPUB;	2003/07/21 15:11
		matrix or grid) near5 (opening or aperture	EPO; JPO;	
		or recess or recessed or wall or protrusion	DERWENT;	
		or protruding or projection or projecting or	IBM TDB	
		depression or divot)		
401	633		USPAT;	2003/07/21 15:22
		(array or panel)	US-PGPUB;	,
		· ·	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
402	192		USPAT;	2003/07/21 15:24
		(array or panel) and (die or array) near4	US-PGPUB;	
		(alignment or aligning or aligned or	EPO; JPO;	
		placement or positioning or placing or	DERWENT;	
1.00		positioned)	IBM_TDB	0000/00/01 15 01
403	119		USPAT;	2003/07/21 15:31
		(array or panel) and (die or array) near2 (alignment or aligning or aligned or	US-PGPUB; EPO; JPO;	
		placement or positioning or placing or	DERWENT;	
		positioned)	IBM TDB	
_	1228	1 -	USPAT;	2003/07/21 09:34
	1220	(array or subarray)) same (package or	US-PGPUB;	2000, 07, 22
		packaging or housing)	EPO; JPO;	
			DERWENT;	
			IBM_TDB	
-	784	'	USPAT;	2003/07/21 09:34
		(array or subarray)) with (package or	US-PGPUB;	
		packaging or housing)	EPO; JPO;	
			DERWENT;	
	100	(4.5.0	IBM_TDB	0000/05/01 10 00
-	186		USPAT;	2003/07/21 10:32
		subarray)) with (package or packaging or housing)	US-PGPUB; EPO; JPO;	
		industry,	DERWENT;	
			IBM TDB	
_	13627	(die or ic or integrated adj circuit or	USPAT;	2003/07/21 10:38
1		chip) near6 (alignment or align or aligning	US-PGPUB;	, , = = = = = = = = = = = = = = = = = =
		or positioning or position or placement or	EPO; JPO;	
		placing or placed or place or locating or	DERWENT;	
		location or locater or aligner or	IBM_TDB	
		positioner) near10 (aperture or hole or wall		
		or projection or protrusion or depression or		
	005	divot or grid or template)	HODAM	2002/07/21 12 42
-	807	(die or ic or integrated adj circuit or	USPAT;	2003/07/21 10:42
		chip) near4 (alignment or align or aligning or positioning or position or placement or	US-PGPUB; EPO; JPO;	
		placing or placed or place or locating or	DERWENT;	
	1	location or locater or aligner or	IBM TDB	
		positioner) near4 (aperture or hole or wall	=====================================	
		or projection or protrusion or depression or		
		divot or grid or template) and (package or		
	1	packaging or housing) and (mirror or mems or		
		optical adj ic or optical near2 circuit or		
	L	laser or diode or led or oled)		

			r'	
-	485	(die or ic or integrated adj circuit or	USPAT;	2003/07/21 11:42
		chip) near4 (alignment or align or aligning	US-PGPUB;	
		or positioning or position or placement or	EPO; JPO;	
		placing or placed or place or locating or	DERWENT;	
		location or locater or aligner or	IBM_TDB	
		positioner) near4 (aperture or hole or wall		
		or projection or protrusion or depression or		
		divot or grid or template) and (die or ic		
		or integrated adj circuit or chip) near4		
		(mount or mounted or mounting or attach or		
		attaching or attachment or attached or		
		adhere or adhered or adhering or affixing or		
		affix or affixed) and (substrate or wafer or		
		lead adj frame) and (package or packaging or		
		housing) and (mirror or mems or optical adj		
		ic or optical near2 circuit or laser or		
	1,175	diode or led or oled)	110000	2002/07/21 11 42
-	1415	(die or ic or integrated adj circuit or	USPAT;	2003/07/21 11:43
		chip) near4 (alignment or align or aligning	US-PGPUB;	
·	i	or positioning or position or placement or	EPO; JPO;	
		placing or placed or place or locating or	DERWENT;	
		location or locater or aligner or	IBM_TDB	
		positioner) near8 (template or matrix or		
	486	grid)	USPAT;	2003/07/21 12:02
-	486	(die or ic or integrated adj circuit or chip) near4 (alignment or align or aligning	US-PGPUB;	2003/01/21 12:02
		or positioning or position or placement or	EPO; JPO;	
		placing or placed or place or locating or	DERWENT;	
		location or locater or aligner or	IBM_TDB	
		positioner) near5 (template or matrix or	1511_106	
		grid) and (housing or package or packaging)		
l _	471	(die or ic or integrated adj circuit or	USPAT;	2003/07/21 12:11
_	4/1	chip) near4 (alignment or align or aligning	US-PGPUB;	2003/07/21 12:11
		or positioning or position or placement or	EPO; JPO;	
		placing or placed or place or locating or	DERWENT;	
		location or locater or aligner or	IBM TDB	
		positioner) near5 (template or matrix or	12122	
		grid) and (housing or package or packaging)		
		not bump adj placement not power adj grid		
_	331		USPAT;	2003/07/21 13:04
		chip) near4 (alignment or align or aligning	US-PGPUB;	,,
		or positioning or position or placement or	EPO; JPO;	
		placing or placed or place or locating or	DERWENT;	
		location or locater or aligner or	IBM TDB	
	1	positioner) near5 (template or matrix or		
		grid) and (housing or package or packaging)		
	1	not bump adj placement not power adj grid		
		not ball adj grid		
-	772	(die or ic or integrated adj circuit or	USPAT;	2003/07/21 13:03
		chip) near4 (alignment or align or aligning	US-PGPUB;	
		or positioning or position or placement or	EPO; JPO;	
		placing or placed or place or locating or	DERWENT;	
		location or locater or aligner or	IBM_TDB	
		positioner) near5 (socket) and (housing or	-	
		package or packaging)		
-	316	(die or ic or integrated adj circuit or	USPAT;	2003/07/21 13:55
		chip) near4 (alignment or align or aligning	US-PGPUB;	
		or positioning or position or placement or	EPO; JPO;	
		placing or placed or place or locating or	DERWENT;	
		location or locater or aligner or	IBM_TDB	
		positioner) near5 (template or matrix or	_	
		grid) and (housing or package or packaging)		
		not bump adj placement not power adj grid	1	
		not ball adj grid not microfluid\$ not fluid\$		
		near3 filter\$		